

Title: WAFER BONDED EPITAXIAL TEMPLATES FOR SILICON **HETEROSTRUCTURES**

Inventor(s): Henry A. ATWATER et al. Appl. No.: 10/784,586

Replacement Sheets

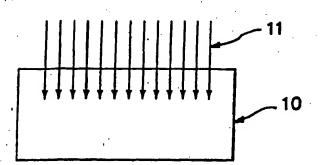
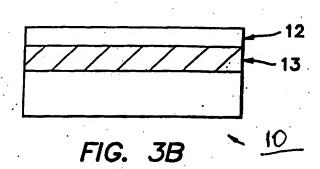


FIG. 3A



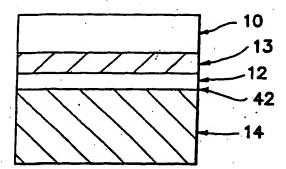
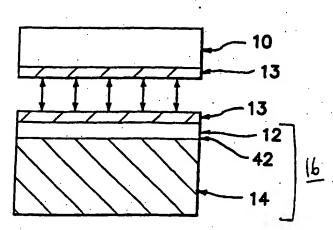


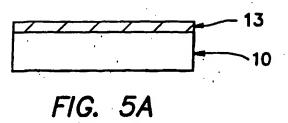
FIG. 4A

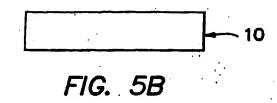
FIG. 4B



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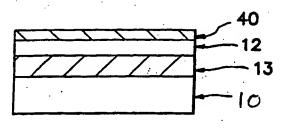


FIG. 6A

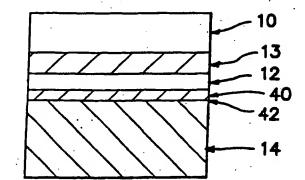
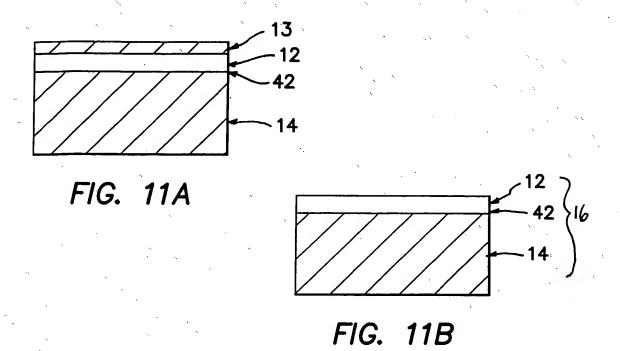


FIG. 6B

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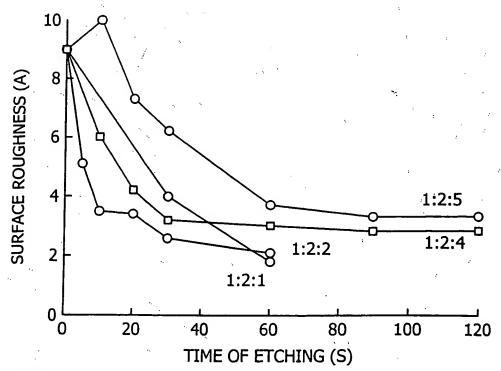


FIG. 12

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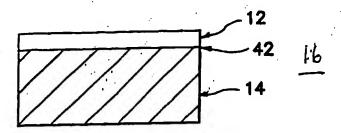


FIG. 13A

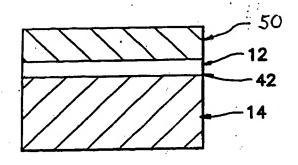


FIG. 13B

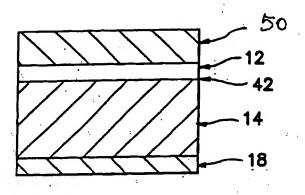


FIG. 14